

## Freeform Search

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**Database:** US Pre-Grant Publication Full-Text Database  
US Patents Full-Text Database  
US OCR Full-Text Database  
EPO Abstracts Database  
JPO Abstracts Database  
Derwent World Patents Index  
IBM Technical Disclosure Bulletins

**Term:** L2 and (nnp adj5 pnp)

**Display:** 10 Documents in **Display Format:** - Starting with Number 1

**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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Search

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### Search History

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**DATE:** Thursday, January 05, 2006   [Printable Copy](#)   [Create Case](#)

**Set Name Query**  
side by side

**Hit Count Set Name**  
result set

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*

<u>L43</u>	L2 and (nnp adj5 pnp)	30	<u>L43</u>
<u>L42</u>	L40 and (nnp adj5 pnp)	31	<u>L42</u>
<u>L41</u>	L40 and (parallel transistors)	15	<u>L41</u>
<u>L40</u>	374/\$.ccls.	28913	<u>L40</u>
<u>L39</u>	parallel transistors	5802	<u>L39</u>
<u>L38</u>	parallel npn pnp	5	<u>L38</u>
<u>L37</u>	parallel pnp npn	6	<u>L37</u>
<u>L36</u>	transistor invers\$3 parallel transistor	0	<u>L36</u>
<u>L35</u>	L34 same (inverse parallel)	0	<u>L35</u>
<u>L34</u>	transistor connection	2373	<u>L34</u>
<u>L33</u>	L31 and (pins or access points)	34	<u>L33</u>
<u>L32</u>	L31 and (n pins)	0	<u>L32</u>
<u>L31</u>	L30 same (transistors)	438	<u>L31</u>
<u>L30</u>	inverse parallel	1738	<u>L30</u>
<u>L29</u>	(inverse parallel) same (transistors) samw (pin\$1)	0	<u>L29</u>
<u>L28</u>	inverse parallel transistors	28	<u>L28</u>

<u>L27</u>	inverse transistor\$1 pair	1	<u>L27</u>
<u>L26</u>	inverse pair\$1 transistor\$1	1	<u>L26</u>
<u>L25</u>	L24 and (n pins)	31	<u>L25</u>
<u>L24</u>	n(n-1)	4686	<u>L24</u>
<u>L23</u>	n(n-1) pairs	1	<u>L23</u>
<u>L22</u>	L21 and (temperature or thermal)	138	<u>L22</u>
<u>L21</u>	L20 and (pins)	294	<u>L21</u>
<u>L20</u>	L19 and (transistors)	1014	<u>L20</u>
<u>L19</u>	257/48	2090	<u>L19</u>
<u>L18</u>	L16 and (temperature or thermal)	160	<u>L18</u>
<u>L17</u>	L15 and (n pins)	0	<u>L17</u>
<u>L16</u>	L15 and (pins)	263	<u>L16</u>
<u>L15</u>	L14 and (transistors)	1254	<u>L15</u>
<u>L14</u>	L13 and (transistors or semiconductors)	3298	<u>L14</u>
<u>L13</u>	438/14	3588	<u>L13</u>
<u>L12</u>	L9 and (pins)	157	<u>L12</u>
<u>L11</u>	L7 and (n pins)	0	<u>L11</u>
<u>L10</u>	L9 and (n pins)	0	<u>L10</u>
<u>L9</u>	L7 and (transistors)	618	<u>L9</u>
<u>L8</u>	L7 and (antiparallel transistors)	0	<u>L8</u>
<u>L7</u>	438/18	1340	<u>L7</u>
<u>L6</u>	L2 and (inverse connection)	0	<u>L6</u>
<u>L5</u>	L2 and (antiparallel)	0	<u>L5</u>
<u>L4</u>	L3 and (antiparallel)	0	<u>L4</u>
<u>L3</u>	L2 and (transistor\$1)	478	<u>L3</u>
<u>L2</u>	327/512	574	<u>L2</u>
<u>L1</u>	antiparallel transistors	38	<u>L1</u>

END OF SEARCH HISTORY

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**Term:**
L18 and (bipolar)

**Display:** 10
**Documents in Display Format:** -
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**Generate:**
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#### Set Name Query

side by side

#### Hit Count Set Name

result set

*DB=USPT; PLUR=YES; OP=ADJ*

L19 L18 and (bipolar) 1 L19

L18 6736540.pn. 1 L18

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*

L17 (thermal sens\$5 transistors) adj5 (integrated circuit or IC or wafer) 0 L17

L16 thermal sens\$5 transistors adj5 chip 0 L16

L15 temperature sens\$5 transistors adj5 chip 7 L15

L14 temperature measur\$3 transistors adj5 IC 0 L14

L13 temperature sens\$5 transistors adj5 IC 2 L13

L12 temperature sens\$4 transistors adj5 IC 2 L12

*DB=EPAB; PLUR=YES; OP=ADJ*

L11 KR-2003042942-A.did. 0 L11

L10 KR-434237-B.did. 0 L10

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*

L9 temperature sens\$4 transistors adj5 integrated circuit 3 L9

L8 transistors adj5 integrated circuit 21414 L8

L7 transistors on IC 0 L7

<u>L6</u>	L1 and (Vbe)	33	<u>L6</u>
<u>L5</u>	L2 and (Vbe)	1	<u>L5</u>
<u>L4</u>	L3 and (Vbe)	5	<u>L4</u>
<u>L3</u>	L1 and (second transistor)	109	<u>L3</u>
<u>L2</u>	L1 and (plurality transistors or plurality semiconductors)	74	<u>L2</u>
<u>L1</u>	374/\$.ccls.	28913	<u>L1</u>

END OF SEARCH HISTORY